

PATENT
051270-0306496

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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|------------------------------------|---|----------------------|
| In re application of: |) | Art Unit: Unassigned |
| |) | Examiner: Unassigned |
| Yukichi ONO |) | |
| |) | |
| Serial No: Unassigned |) | |
| |) | |
| Filed: October 30, 2003 |) | |
| |) | |
| For: SEMICONDUCTOR INTEGRATED |) | |
| CIRCUIT DEVICE AND ITS MANUFACTURE |) | |
| USING AUTOMATIC LAYOUT |) | |

PRELIMINARY AMENDMENT

Mail Stop Patent Application
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Please consider the following amendments and remarks prior to examination of the above-identified patent application.